

	Type	L #	Hits	Search Text	DBs	Time Stamp
4	BRS	L4	5	((manufactur\$3 adj2 (option configuration)) and (interconnect\$3 (conductive adj patter) rout\$3 wir\$3) and control\$3).clm.	US-PGPUB	2005/10/17 08:04
5	BRS	L5	1	((ram and rom) and (layer substrate) and (manufactur\$3 adj2 (option configuration))).clm.	US-PGPUB	2005/10/17 08:06
6	BRS	L6	1	(((ram rom) near2 (option mode configuration)) and (non\$2planar planar substrate) and data).clm.	US-PGPUB	2005/10/17 08:09
7	BRS	L7	59	(((ram rom) near2 (option mode configuration)) and data).clm.	US-PGPUB	2005/10/17 08:09